

COMMUNICATIONS RECEIVERS AND METHODS THEREFOR

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FIELD OF THE INVENTIONS

The present inventions relate generally to communications receivers, and more particularly to receivers having reduced intermodulation distortion for mobile communications handsets, combinations thereof and methods therefor.

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BACKGROUND OF THE INVENTIONS

Direct conversion receivers convert signals directly to baseband for subsequent processing and thus eliminate the use of converters and intermediate filters typical of other receivers, for example those of superheterodyne receivers. Intermediate frequency receivers, for example very low intermediate frequency (VLIF) receivers, convert signals to an intermediate frequency for processing before converting to baseband.

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Direct conversion receivers are less complex and less costly relative to intermediate frequency receivers, and thus there is a strong desire to implement direct conversion receivers in communications devices, for example in mobile cellular communications handsets.

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The Universal Mobile Telecommunications Service (UMTS) wide band code division multiple access (WCDMA) communication standard has frequency spectrum allocated exclusively to WCDMA communications. The exclusively allocated WCDMA frequency spectrum is substantially devoid of narrow band blockers. Other non-exclusively allocated frequency bands used for WCDMA communications, however, are subject to high-level narrowband

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blockers, which produce 2nd and higher order intermodulation distortion products, which degrade receiver performance.

The distortion may be mitigated in direct conversion receivers with a high 2nd order intercept mixer, as described for example in U.S. Patent No. 6,192,225 entitled "Direct Conversion Receiver" and in U.S. Patent No. 6,125,272 entitled "Method And Apparatus Improved Intermodulation Distortion Protection", and by baseband frequency filtering, but these techniques are often insufficient for many applications.

Distortion may be reduced in intermediate frequency receivers by filtering the signal before conversion to baseband. Filtering before conversion to baseband however works well only for applications where the baseband sampling rate is sufficient to accommodate the intermediate frequency offset and where adjacent channel rejection requirements are less severe.

The various aspects, features and advantages of the present invention will become more fully apparent to those having ordinary skill in the art upon careful consideration of the following Detailed Description of the Invention with the accompanying drawings described below.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is frequency domain graphical illustration of various signals and signal components in the receivers of the present invention.

FIG. 2 is an exemplary RF receiver schematic block diagram according to an exemplary embodiment of the invention.

FIG. 3 is an exemplary radio communication device having a receiver for performing the methods of the present invention.

FIG. 4 is an exemplary flow diagram for a process in an RF receiver.

DETAILED DESCRIPTION OF THE INVENTIONS

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FIG. 1 illustrates various input signals and filter responses for a non-linear receiver illustrated in the block circuit diagram of FIG. 2. The signal (S_1) is a desired signal and the signal (S_2) is an interference signal, or a blocker, which when received and processed by the non-linear receiver produces distortion, for example 2nd and higher order intermodulation distortion products.

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In one exemplary embodiment, the receiver is implemented as either a direct conversion or intermediate frequency receiver, for example a very low intermediate frequency (VLIF) receiver, in a radio communication device illustrated in FIG. 3. The exemplary radio communication device comprises generally a processor 30 coupled to memory 32, a transmitter/receiver 34, input/output devices 36 and a display 38, and in one embodiment the radio communication device is a cellular communication handset.

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In FIG. 2, the desired signal (S_1) and the blocker (S_2) are received at an antenna coupled to an amplifier stage 20, which is coupled to a mixer 21 and thereafter a non-linear stage 22. As noted, signal distortion products are produced by the blocker signal at the mixer and non-linear stages of the receiver.

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Generally, the signal distortion products include narrowband and wideband intermodulation distortion products. In one exemplary mode of operation, the radio communication handset receiver receives a wideband signal in the presence of narrowband blockers, for example by receiving in WCDMA band susceptible to GSM narrowband blockers. In this exemplary application, it is

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desirable to suppress the narrowband blockers. More generally, however, the methods of the invention may be used to suppress any distortion products.

FIG. 1 illustrates two 2nd order distortion products ($\frac{1}{2} S_2^2$), one of which is within the desired signal bandwidth at or near the center frequency f_{cf} . In a direct conversion receiver, the center frequency (f_{cf}) is 0 Hz, but the center frequency could be at any frequency, for example that typical of an intermediate conversion receiver. FIG. 1 also illustrates thermal noise ($kTBF$), which is pervasive.

FIG. 1 illustrates the passband (H_1) for a first filter, which is a low pass filter (LPF) in the exemplary embodiment of FIGS. 1 and 2, passing the desired signal (S_1) and an undesired distortion product, which is a lower band 2nd order distortion product ($\frac{1}{2} S_2^2$) in the exemplary embodiment.

In one embodiment of the invention, illustrated in the process flow diagram of FIG. 4, at block 40, power (P_{DIST}) is determined for the undesired signal distortion products within the passband of the desired signal S_1 . In the exemplary embodiment, the distortion product is the lower band 2nd order intermodulation distortion product ($\frac{1}{2} S_2^2$) illustrated in FIG. 1. In other embodiments, the power of other undesired distortion products in the first filter passband (H_1) may also be determined.

In FIG. 4, at block 41, power ($P_{DS+DIST}$) for both the desired signal and the undesired distortion products is also determined. In FIG. 2, power determinations including (P_{DS}) and ($P_{DS+DIST}$), are made at receiver circuit block 23.

Generally, the distortion products are filtered by dynamically selecting one or more filter properties. In one embodiment, for example, the distortion products are filtered by dynamically adjusting the bandwidth of rejection of an infinite impulse response (IIR) filter by selecting corner frequencies, and in another embodiment the distortion products are filtered by dynamically

adjusting the rejection of a finite impulse response (FIR) filter by controlling filter coefficients, without adjusting the bandwidth of rejection. In other embodiments, both the bandwidth of rejection and the rejection of the filter are adjusted dynamically. In other alternative embodiments, the properties of other filters may be adjusted dynamically.

In the exemplary embodiment of FIGS. 1 and 2, the distortion products are filtered by a second filter having a second passband (H_2) and a third filter having a third passband (H_3). In FIGS. 1 and 2, the second filter passband is that of a low pass filter and the third passband is that of a band-pass filter. In other embodiments, however other filters and/or filter passband combinations may be used alternatively, for example a single filter may be used. Dynamic adjustment of the filter rejection occurs at digital signal processing block, for example block 24 in FIG. 2.

Generally, the rejection properties of the filter are adjusted dynamically as a function of the power for both the desired signal and the undesired signal distortion products, ($P_{DS+DIST}$). In FIG 4, at block 43, the rejection properties, for example the bandwidth of rejection of the filter rejection is selected based upon the power ($P_{DS+DIST}$), for example from a look-up table stored in memory on the receiver. In the exemplary circuit of FIG. 2, for example, the corner frequencies of the second filter passband (H_2) and the third filter passband (H_3) are selected at block 43 in FIG. 4. In FIG. 2, a signal processing block 24 provides bandwidth or rejection frequency control signals to second and third filter blocks 25 and 26 based on the power ($P_{DS+DIST}$) determined at block 23.

In one embodiment, in FIG. 4, at block 45, a determination is made whether a ratio of powers exceeds a predetermined threshold. The ratio of powers, determined at block 42, is the power for the signal distortion products (P_{DS}) divided by the power for both the desired signal and the signal distortion

products ($P_{DS+DIST}$). In the exemplary embodiment of FIG. 2, the ratio of powers
power ($P_{DS}/P_{DS+DIST}$) is determined at block 23. At block 47, the filter rejection
properties are selected based upon whether the ratio of powers is above the
predetermined threshold, as determined at block 45, in FIG. 4. The threshold is
5 selected at block 46 and is a function the power ($P_{DS+DIST}$), for example from a set of
thresholds stored in a look-up table.

In one embodiment where the ratio of powers is compared to the
threshold at block 45, the filter rejection properties is set equal to none, at block 47
in FIG. 4, for example, the bandwidth of rejection or the rejection is set to zero, if
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In another embodiment where the ratio of powers is compared to the
threshold at block 45, the rejection properties are set at a fixed value, for example a
fixed bandwidth of rejection or fixed filter rejection, at block 43 if the ratio of
powers is above the predetermined threshold. In another embodiment, the
rejection properties are adjusted dynamically at block 43 when the ratio of powers
15 is above the predetermined threshold.

In other embodiments, the filter rejection properties are adjusted
dynamically without regard for whether the power ratio exceeds the threshold. In
this embodiment, the rejection properties are selected at block 43 in FIG. 4 based
20 upon the power of both the desired signal and the distortion products, ($P_{DS+DIST}$). In
FIG. 4, the rejection properties are set at block 44.

While the present inventions and what is considered presently to be
the best modes thereof have been described in a manner that establishes
possession thereof by the inventors and that enables those of ordinary skill in the
25 art to make and use the inventions, it will be understood and appreciated that
there are many equivalents to the exemplary embodiments disclosed herein and
that myriad modifications and variations may be made thereto without departing

from the scope and spirit of the inventions, which are to be limited not by the exemplary embodiments but by the appended claims.

What is claimed is: